



YOGYAKARTA STATE UNIVERSITY  
 FACULTY OF MATHEMATICS AND NATURAL SCIENCES

SYLLABI

FRM/FMIPA/063-00  
 1 April 2010

Faculty : Mathematics and Natural Sciences  
 Study Program : Physics and Physics Education  
 Course / Code : Analog Electronics/FIS 313  
 Credit : Theory: 3 SKS Practice: 1 sks  
 Semester : 3<sup>th</sup> (physics & physics education study program)  
 Prerequisite/Code : Electricity Circuit Analysis/FIS 211  
 Lecturer : Pujiyanto, M.Pd.

I. Course Description

This course studies concept and the principle of analog electrostatics such as direct current circuit, alternating current circuit, semiconductor, semiconductor diode, rectifying circuit, concept of amplification, bipolar transistor amplifiers, field effect amplifiers and their application in daily life.

II. Standard of Competence

After conducting this course, the students can understand analog electronics concepts and has ability to apply in the daily life.

III. Activity

Meeting#	Basic Competence	Essentials Concept	Learning Strategy	Learning Materials/References	Character
1 <sup>st</sup>	Introduction	<ul style="list-style-type: none"> <li>Syllaby</li> <li>Course Contract</li> </ul>	Discussion and information	Syllaby	responsible
2 <sup>nd</sup>	To understand the principle of analog electronics	<ul style="list-style-type: none"> <li>Semiconductor</li> <li>Diodes</li> <li>Junction Diodes</li> </ul>	Discussion and information	A1, A2	Confident complying, appreciating
3 <sup>th</sup>	To understand the characteristics of semiconductor	<ul style="list-style-type: none"> <li>Intrinsic Semiconductor</li> <li>Extrinsic Semiconductor</li> <li>N-type Semiconductor</li> <li>P-type Semiconductor</li> </ul>	Discussion and information	A1, A2, B2	Responsible, thinking logically, creatively, inovatively,
4 <sup>th</sup> , 5 <sup>th</sup> ,6 <sup>th</sup>	Junction Diodes	<ul style="list-style-type: none"> <li>N-type material</li> <li>P-type material</li> <li>PN Junction</li> </ul>	Discussion and information	A1, A2,B3	Responsible, thinking logically,

		<ul style="list-style-type: none"> <li>• Forwarded-Biased Junction</li> <li>• Reverse-Biased Junction</li> </ul>			creatively, inovatively,
7 <sup>th</sup> ,8 <sup>th</sup>	Rectifying Circuits and DC Power Supplies (1 <sup>st</sup> )	<ul style="list-style-type: none"> <li>• Load Line Analysis of Diode Circuit</li> <li>• The Half-wave Rectifier</li> <li>• Voltage Regulation</li> <li>• Ripple Factor</li> <li>• Ratio of Rectifications</li> <li>• TUF</li> </ul>	Discussion and information	A1, A2	Responsible, thinking logically, creatively, inovatively
9 <sup>th</sup>	Mid Term				
10 <sup>th</sup>	Rectifying Circuits and DC Power Supplies (2 <sup>nd</sup> )	<ul style="list-style-type: none"> <li>• The Full-wave Rectifier</li> <li>• The Bridge Rectifier</li> <li>• Comparison of Rectifier Circuits</li> </ul>	Discussion and information	A1, A2,B2	Responsible, thinking logically, creatively, inovatively appreciating
11 <sup>th</sup>	Zener Diodes	<ul style="list-style-type: none"> <li>• Zener Diode Specifications</li> <li>• The Voltage Regulator Circuit</li> <li>• Design of Voltage Regulator Circuit</li> <li>• Effect of Supply Voltage Variations</li> <li>• Zener Diode Breakdown Mechanism</li> <li>• Reference Zener Diode</li> </ul>	Discussion and information	A1,A2, B1, B3	Responsible, thinking logically, creatively, inovatively appreciating
12 <sup>th</sup>	General Amplifier Characteristics	<ul style="list-style-type: none"> <li>• Concept of Amplification</li> <li>• Amplifier Notation</li> <li>• Current Gain, <math>A_i</math></li> <li>• Voltage Gain, <math>A_v</math></li> <li>• Power Gain, <math>A_p</math></li> </ul>	Discussion and information	A1, A2, B3	Responsible, thinking logically, creatively, inovatively, dicipline,

		<ul style="list-style-type: none"> <li>• Amplifier Input Resistance, <math>R_i</math></li> <li>• Amplifier Output Resistance, <math>R_o</math></li> </ul>			curious
13 <sup>th</sup> , 14 <sup>th</sup>	Bipolar Transistor Amplifier	<ul style="list-style-type: none"> <li>• Basic characteristics of the Transistor</li> <li>• Basic Transistor Amplifier</li> <li>• Transistor Input Characteristics</li> <li>• Transistor Collector Characteristics, CE</li> <li>• Collector Cutoff Current, <math>I_{CEO}</math></li> <li>• Forward Current Transfer Ratio, CE</li> </ul>	Discussion and information	A, B1, B2	Responsible, thinking logically, creatively, inovatively, dicipline, curious
15 <sup>th</sup>	The Common-Base Amplifier and The Common-Emitter Amplifier	<ul style="list-style-type: none"> <li>• The Basic Common-Base Amplifier</li> <li>• Forward Current Transfer Ratio, CB</li> <li>• Relation Between <math>\alpha</math> and <math>\beta</math></li> <li>• Analysis of the Common-Base Amplifier</li> <li>• Power Relationships</li> <li>• Efficiency</li> <li>• Graphical Analysis of a Common-Emmitter Class A Amplifier</li> <li>• Input and Output Resistance</li> <li>• Effect of Adding an AC Load</li> <li>• Phase Relationships in a CE Amplifier</li> </ul>	Discussion and information	A1, A2,B3	Responsible, thinking logically, creatively, inovatively, dicipline, curious
16 <sup>th</sup>	Field Effect Transistors	<ul style="list-style-type: none"> <li>• Advantages and Disadvantages of the FET</li> </ul>		A1, A2	

		<ul style="list-style-type: none"> <li>• Basic Construction of the JFET</li> <li>• Characteristic Curves of the JFET</li> <li>• Principle of Operation of the JFET</li> <li>• Frequency Response of the FET Amplifier</li> </ul>			
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#### IV. References

##### A, Compulsory:

1. Mottershead, A., 1981. *Electronic Device and Circuits, An Introduction*. New Delhi : Prentice Hall of India.
2. Sutrisno, 1986. *Elektronika Teori dan Penerapannya 1*. Bandung : Penerbit ITB.

##### B. Additional:

1. Milman, J. & Halkias, C.C., (1971), *Integrated Electronics*, New York : McGraw Hill Inc.
2. Mehta, V.K., (1997), *Principles of Electronics*, New Delhi : S. Chand & Company LTD.
3. Brophy, J.J., (1983), *Basic Electronics for Scientists*, New York : McGraw Hill Book Company.

#### V. Evaluation

No	Componen	Worth
1	Participation	10 %
2	Assignment	30 %
3	Midterm Exam	30%
4	Final Exam	30%
		100%

Yogyakarta, August 16<sup>th</sup>, 2010

Pujianto, M.Pd.